



Open Accessibility  
Everywhere

Groundwork, Infrastructure, Standards

## ÆGIS 1<sup>st</sup> Pan-European Workshop

*‘Accessibility for All: Open Source-based Generalised  
Accessibility Support for Mainstream ICT  
Devices/Applications’*

**5<sup>th</sup> June 2009, London, UK**

### INVITATION

Dear Madam/Sir,

Having affordable accessibility support into mainstream ICT devices/applications is much wanted in the disability community. Accessible devices and applications offer the backbone to many people with disabilities across Europe and beyond to maintain their autonomy as this is associated to a better quality of life.

Supported by the 7<sup>th</sup> Framework Programme on Research and Technological Development of the European Union, the ÆGIS (Open Source-based Generalised Accessibility Support for Mainstream ICT) project aims to develop an Open Accessibility Framework (OAF) through which aspects of the design, development and deployment of accessible mainstream ICT are addressed. The OAF will provide developers with embedded and built-in accessibility solutions, as well as toolkits for “engraving” accessibility in existing and emerging mass-market ICT-based products, thus making accessibility open, plug and play, personalised and configurable, realistic and applicable in various contexts. Simultaneously, the project will provide Open Source Accessible Desktop Applications to the end-users (people with disabilities, older people)

On behalf of the ÆGIS Consortium, we kindly invite you to participate in its first Pan-European Workshop that will take place on Friday 5<sup>th</sup> June 2009 at Research In Motion UK Ltd, Slough, Berkshire, United Kingdom.

This workshop will present the preliminary findings of the project related to the Use Cases selection and preliminary user requirements, thus stimulating discussions on these issues between both end-users and application developers. Its outcome will be taken into account by the Consortium during the further course of the project. Key stakeholders from relevant areas are expected to participate (such as end users’ representatives and organisations, EC representatives, technologies developers, etc.). This workshop therefore is an excellent chance to meet experts from relevant fields.



Contact: [info@aegis-project.eu](mailto:info@aegis-project.eu)  
Web site: <http://www.aegis-project.eu/>



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Participation to the ÆGIS workshop is **free of charge**. However, due to space limitation, participation will be on a first-come first-served basis. We would appreciate it if you could **return the enclosed registration form before Thursday, 30 April 2009**.

For further information about the ÆGIS project, please visit the ÆGIS website at [www.aegis-project.eu](http://www.aegis-project.eu).

We are looking forward to welcome you on the 5<sup>th</sup> of June!

Yours sincerely,

**Dr. Evangelos Bekiaris**  
ÆGIS Coordinator

**Peter Korn**  
ÆGIS Technical Manager

### Agenda

Time	Topic	Speaker
8.30 – 9.00	Registration	
9.00 – 9.10	Welcome and Aim of the Day	Mr. Karel Van Isacker (EPR)
9.10 – 9.35	EU Policies on eINCLUSION	Mr. Miguel Gonzalez-Sancho (European Commission, ICT for Inclusion, Information Society & Media Directorate General )
9.35 – 9.50	ÆGIS at a Glance	Dr. Evangelos Bekiaris (CERTH-HIT)
9.50 – 10.10	First feedback as collected during user forum	Mr. Iván Carmona Rojo (FONCE)
10.10 – 10.40	Demos and Technical Overview	Mr. Peter Korn (Sun Microsystems)
10.40 – 11.00	Coffee Break	
11.00 – 12.30	ÆGIS Draft Use Cases and discussion	Dr. Evangelos Bekiaris (CERTH-HIT)
12.30 – 14.00	Lunch	
14.00 – 14.30	Standards for content (and content interoperability) in the field of eInclusion - and beyond	Dr. Christian Galinski (Infoterm)
14.30 – 15.00	Accessibility overall	Ph.D. Gregg C. Vanderheiden (Trace R&D Center University of Wisconsin - Madison)
15.00 - 15.30	Coffee break	
15.30 – 15.45	Kick off of Open Accessibility Everywhere Group (OAEG)	Dr. Evangelos Bekiaris (CERTH-HIT)
15.45 – 16.30	Round table	Moderated by Dr. Evangelos Bekiaris (CERTH-HIT) and Mr. Peter Korn (Sun Microsystems)
16.30 – 17.00	Conclusions	



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